Notice of References Cited

Application/Control No. 10/649,765	Applicant(s)/Patent Under Reexamination OBARA, TERUHISA		
Examiner	Art Unit		
Saqib J. Siddiqui	· 2138	Page 1 of 1	

U.S. PATENT DOCUMENTS

*	i	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-6,014,763 A	01-2000	Dhong et al.	714/724
*	В	US-6,169,500 B1	01-2001	Eriksson et al.	341/100
*	С	US-6,242,269 B1	06-2001	Whetsel, Lee D.	438/11
*	D	US-6,466,247 B1	10-2002	Sugano et al.	347/250
*	E	US-2003/0056183 A1	03-2003	Kobayashi, Munenori	716/4
*	F	US-5,926,120	07-1999	Swenson et al.	341/100
*	G	US-5,012,240	04-1991	Takahashi et al.	341/101
*	Н	US-5,247,652	09-1993	Uda, Yoshihiro	710/71
*	1	US-5,119,379	06-1992	Dara, Paul S.	714/46
*	J	US-4,499,454	02-1985	Shimada, Toshiyuki	341/58
	K	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0	•				
	Р					
•	Q				·	
	R					<u> </u>
	S			•		
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	PHILIPS, 74HC/HCT166 8-bit parallel-in/serial-out shift register December 1990, Philips Semiconductor, pgs 4-6.
	٧	
	w	
	x	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

· U.S. Patent and Trademark Office PTO-892 (Rev. 01-2001)